SYSTEM AND METHOD FOR SWITCHING LOW LATENCY HIGH PRIORITY TRAFFIC

Kenneth M. Rose David S. Walker Michael A. Benning Mick R. Jacobs

ABSTRACT OF THE DISCLOSURE

[0052] An apparatus including a first buffer, a second buffer and a priority switch circuit. The first buffer is configured to store data of a first data stream having a first priority. The second buffer is configured to store data of a second data stream having a second priority. The priority switch circuit is coupled to the first buffer and the second buffer and is configured to interrupt a transmission of the first data stream from the first buffer upon detection of data of the second data stream and transmit data of the second data stream